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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Perino *et al.* Confirmation No.: 4507
Serial No.: 09/961,114 Art Unit: 2833
Filed: September 20, 2001 Examiner: Figueroa, Felix O.
For: *Chip Socket Assembly
and Chip File Assembly
for Semiconductor Chips* Attorney Docket No: 60809-5054-US

PETITION FOR EXTENSION OF TIME UNDER 37 CFR § 1.136(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

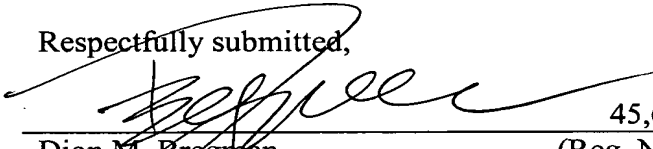
It is respectfully requested that the time for response to the final Office Action dated March 21, 2005 be extended for a period of one month, from June 21, 2005 to and including July 21, 2005.

The fee for this extension is estimated to be \$120.00. Please charge the required fee to Morgan, Lewis & Bockius LLP Deposit Account No. 50-0310 (order no. 60809-5054-US).

A copy of this sheet is enclosed.

Date: July 21, 2005

Respectfully submitted,


Dion M. Bregman
MORGAN, LEWIS & BOCKIUS LLP
2 Palo Alto Square
3000 El Camino Real, Suite 700
Palo Alto, California 94306
(650) 843-4000

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(Reg. No.)

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